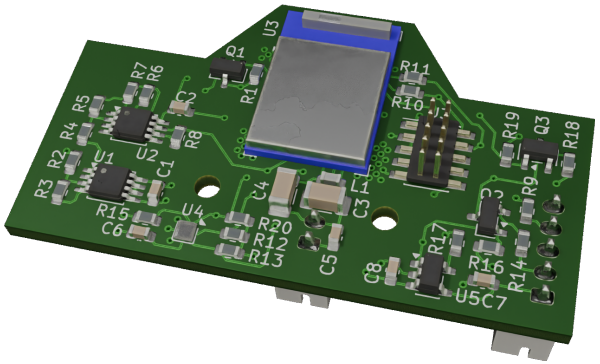
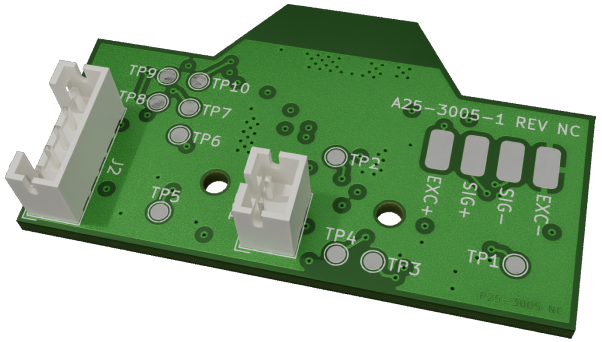
| REVISION HISTORY | | |
|---|---|---|
| REV | DESCRIPTION | ZONE |
| | | |

Top View




Bottom View




# The Git Edit

| | | | |
|---|---|---|---|
| Drawing Title: PCB, Assembly Name | | | |
| Size: A4 | Drawing Number: P99-9000 | | Revision: |
| Designer: JPK | File Name: Test_5.kicad_pcb | | |
| Used On | Release State: WORKING | Release Date | Git Hash: 17ccbe1 |
| Used On: A99-9000 | Scale: 2:1 | ${KICAD_VERSION} | Sheet: of 1 |

| REVISION HISTORY | | |
|---|---|---|
| REV | DESCRIPTION | ZONE |
| | | |

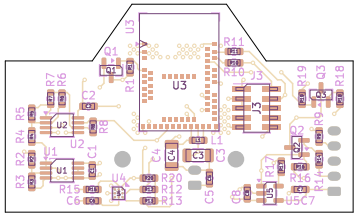
ASSEMBLY NOTES (UNLESS OTHERWISE SPECIFIED)

1) DO NOT POPULATE COMPONENTS ARE MARKED WITH A RED CROSS

2) DO NOT POPULATE COMPONENTS ARE NOT PRESENT IN THE BOM

3) IF CONFLICTING INFORMATION IS FOUND BETWEEN THE ASSEMBLY FILE AND BOM, BOM SHOULD BE USED AS THE MAIN SOURCE.

4) DOT IDENTIFIES PIN #1 LOCATION AND DEVICE ORIENTATION WHEN VIEWED FROM THE TOP.

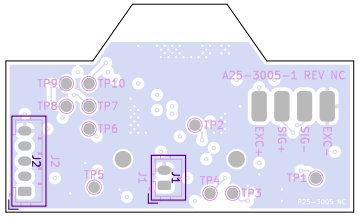


| Type | Front Side | Back Side | Total |
|---|---|---|---|
| THT | 0 | 2 | 2 |
| SMD | 38 | 0 | 38 |
| Total | 38 | 2 | 40 |

# The Git Edit

| | | | |
|---|---|---|---|
| Drawing Title: PCB, Assembly Name | | | |
| Size: A4 | Drawing Number: P99-9000 | | Revision: |
| Designer: JPK | File Name: Test_5.kicad_pcb | | |
| Used On: | Release State: WORKING | Release Date: | Git Hash: 17ccbe1 |
| Used On: | Scale: 2:1 | ${KICAD_VERSION} | Sheet: of 1 |
| Used On: A99-9000 | | | |

| REVISION HISTORY | | |
|---|---|---|
| REV | DESCRIPTION | ZONE |
| | | |




# The Git Edit

| | | | |
|---|---|---|---|
| Drawing Title: PCB, Assembly Name | | | |
| Size: A4 | Drawing Number: P99-9000 | | Revision: |
| Designer: JPK | File Name: Test_5.kicad_pcb | | |
| Used On | Release State: WORKING | Release Date | Git Hash: 17ccbe1 |
| Used On: A99-9000 | Scale: 2:1 | ${KICAD_VERSION} | Sheet: of 1 |